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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

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Serial No.: 10/828,958

Filed: April 20, 2004

For: A METHOD FOR MAKING A  
SEMICONDUCTOR DEVICE  
HAVING A HIGH-K GATE  
DIELECTRIC LAYER AND A  
METAL GATE ELECTRODE

Art Unit: 2812

Examiner: unknown

Attorney Docket: P18609

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

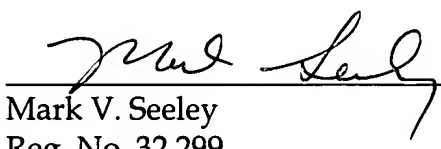
This Information Disclosure Statement is being submitted under 37 C.F.R. §1.97(b). Enclosed is a copy of Information Disclosure Citation Form PTO-1449 together with copies of the references cited on that form. It is respectfully requested that the cited references be considered and that the enclosed copy of Information Disclosure Citation Form PTO-1449 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

Pursuant to 37 C.F.R. § 1.97, the submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made

and is not to be construed as an admission that the information cited in this statement constitutes prior art or is otherwise material to patentability.

Respectfully submitted,

Dated: Nov. 22, 2004

  
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(37 C.F.R. § 1.8(a))

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Date

<b>Form PTO-1449 (Modified)</b>		<b>Atty Docket No.:</b> P18609		<b>Serial No.:</b> 10/828,958		
<b>List of Patents and Publications Statement</b> (Use several sheets if necessary)				<b>Applicant:</b> Justin K. Brask et al.		
				<b>Filing Date:</b> April 20, 2004		

NOV 23 2004

REFERENCE DESIGNATION		U.S. PATENT DOCUMENTS				
Examiner Initials		Document No.		Class	Sub-Class	Filing date if appropriate
	AA	6,642,131 B2	Harada	438	591	
	AB	6,667,246 B2	Mitsuhashi et al.	438	756	
	AC	6,794,234 B2	Polishchuk et al.	438	199	
	AD					
	AE					
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FOREIGN PATENT DOCUMENTS							
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OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)		
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<b>Examiner</b>	<b>Date Considered</b>
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